

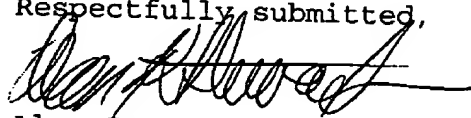
REMARKS

Claims 1-25 are pending in the application. Claims 1-25 are rejected.

Claims 1-25 were rejected under 35 USC 102(b) as being anticipated by Levinson. Claims 1, 10, 15, and 20 include "... a single-stage inverting amplifier ... to receive ... charge-coupled inputs, and to amplify said inputs with reference to said second voltage, and to drive said analog input accordingly." The references of record do not show, teach, or suggest the above limitation of the claims. Levinson does not show, teach, or suggest using the second reference voltage of the analog-to-digital converter for a single-stage inverting amplifier to amplify with reference to. Claims 2-9 depend from claim 1. Claims 11-14 depend from claim 10. Claims 16-19 depend from claim 15. Claims 21-25 depend from claim 20. Therefore, claims 1-25 are believed to be allowable over the references of record

It is believed that the above remarks are fully responsive to the Official Action. Reconsideration and allowance are therefore respectfully requested.

Respectfully submitted,



Alan Stewart  
Registration No. 35,373

Texas Instruments, Incorporated  
P. O. Box 655474 - M/S 3999  
Patent Department  
Dallas, Texas 75265